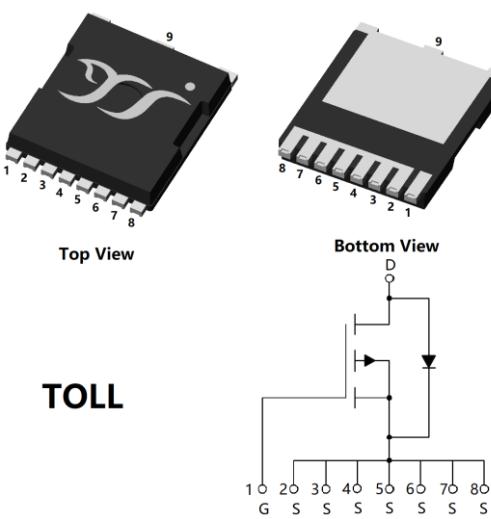


## P-Channel Enhancement Mode Field Effect Transistor



### Product Summary

- $V_{DS}$  -80V
- $I_D$  -61A
- $R_{DS(ON)}$  (at  $V_{GS}=-10V$ ) <19mΩ
- 100% EAS Tested
- 100%  $\nabla V_{DS}$  Tested

### General Description

- Low  $R_{DS(ON)}$  & FOM
- Excellent stability and uniformity
- Moisture Sensitivity Level 1
- Epoxy Meets UL 94 V-0 Flammability Rating
- Halogen Free

### Applications

- Power management
- Portable equipment

### ■ Limiting Values

Parameter	Conditions		Symbol	Min	Max	Unit
Drain-source Voltage			$V_{DS}$	-	-80	V
Gate-source Voltage			$V_{GS}$	-20	20	
Continuous Drain Current (Note 1,2)	Steady-State	$T_A=25^\circ C, V_{GS}=-10V$	$I_D$	-	-9	A
		$T_A=100^\circ C, V_{GS}=-10V$		-	-5.7	
Continuous Drain Current (Note 1,3)		$T_C=25^\circ C, V_{GS}=-10V$ , Chip limitation		-	-61	
		$T_C=100^\circ C, V_{GS}=-10V$		-	-38	
Pulsed Drain Current	$T_C=25^\circ C, t_p \leq 10\mu s$		$I_{DM}$	-	-244	W
Maximum Body-Diode Continuous Current	$T_C=25^\circ C$		$I_S$		-61	
Avalanche energy (non-repetitive)	$T_J=25^\circ C, V_G=-10V, R_G=25\Omega, L=0.5mH, I_{AS}=-38.5A$		EAS	-	370.5	
Total Power Dissipation (Note 1,2)	Steady-State	$T_A=25^\circ C$	$P_D$	-	3	
		$T_A=100^\circ C$		-	1.2	
Total Power Dissipation (Note 1,3)	Steady-State	$T_C=25^\circ C$		-	138	W
		$T_C=100^\circ C$		-	55	
Junction and Storage Temperature Range			$T_J, T_{STG}$	-55	150	°C

### ■ Thermal Resistance

Parameter	Symbol	Typ	Max	Units
Thermal Resistance Junction-to-Ambient (Note 2)	$R_{\theta JA}$	-	41	°C/W
Thermal Resistance Junction-to-Case	$R_{\theta JC}$	-	0.9	

### ■ Ordering Information (Example)

PREFERRED P/N	PACKING CODE	Marking	MINIMUM PACKAGE(pcs)	INNER BOX QUANTITY(pcs)	OUTER CARTON QUANTITY(pcs)	DELIVERY MODE
YJT019GP08A	F1	YJT019GP08A	2000	4000	20000	13" reel



## ■ Electrical Characteristics

Parameter	Symbol	Conditions	Min	Typ	Max	Units
<b>Static Parameter</b>						
Drain-Source Breakdown Voltage	$BV_{DSS}$	$V_{GS}=0V, I_D=-250\mu A, T_j=25^\circ C$	-80	-	-	V
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS}=-80V, V_{GS}=0V, T_j=25^\circ C$	-	-	-1	$\mu A$
		$V_{DS}=-80V, V_{GS}=0V, T_j=125^\circ C$	-	-	-100	
Gate-Source Leakage Current	$I_{GSS}$	$V_{GS}=\pm 20V, V_{DS}=0V, T_j=25^\circ C$	-	-	$\pm 100$	nA
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=-250\mu A, T_j=25^\circ C$	-1.2	-2.1	-3	V
Static Drain-Source On-Resistance	$R_{DS(ON)}$	$V_{GS}=-10V, I_D=-30A, T_j=25^\circ C$	-	14	19	$m\Omega$
Diode Forward Voltage	$V_{SD}$	$I_S=-30A, V_{GS}=0V, T_j=25^\circ C$	-	-0.87	-1.2	V
Gate Resistance	$R_G$	$f=1MHz, T_j=25^\circ C$	-	8.5	-	$\Omega$
<b>Dynamic Parameters</b>						
Input Capacitance	$C_{iss}$	$V_{DS}=-40V, V_{GS}=0V, f=1MHz, T_j=25^\circ C$	-	5225	-	$pF$
Output Capacitance	$C_{oss}$		-	483	-	
Reverse Transfer Capacitance	$C_{rss}$		-	16.6	-	
<b>Switching Parameters</b>						
Total Gate Charge	$Q_g$	$V_{GS}=-10V, V_{DS}=-40V, I_D=-30A, T_j=25^\circ C$	-	90	-	$nC$
Gate-Source Charge	$Q_{gs}$		-	11.9	-	
Gate-Drain Charge	$Q_{gd}$		-	11.8	-	
Reverse Recovery Charge	$Q_{rr}$	$I_F=-30A, di/dt=100A/\mu s, V_{GS}=0V, V_R=-40V, T_j=25^\circ C$	-	125	-	$nC$
Reverse Recovery Time	$t_{rr}$		-	56	-	ns
Turn-on Delay Time	$t_{D(on)}$		-	12	-	ns
Turn-on Rise Time	$t_r$	$V_{GS}=-10V, V_{DS}=-40V, I_D=-30A, R_{GEN}=3\Omega, T_j=25^\circ C$	-	50	-	
Turn-off Delay Time	$t_{D(off)}$		-	165	-	
Turn-off Fall Time	$t_f$		-	69	-	

### Note:

- The entire application environment impacts the thermal resistance values shown, they are not constants and are only valid for the particular conditions noted.
- The value of  $R_{\theta JA}$  is measured with the device mounted on the 40mm\*40mm\*1.1mm single layer FR-4 PCB board with 1 in<sup>2</sup> pad of 2oz. Copper, in the still air environment with  $T_A=25^\circ C$ . The maximum allowed junction temperature of 150°C. The value in any given application depends on the user's specific board design.
- Thermal resistance from junction to soldering point (on the exposed drain pad).

## ■ Typical Electrical and Thermal Characteristics Diagrams

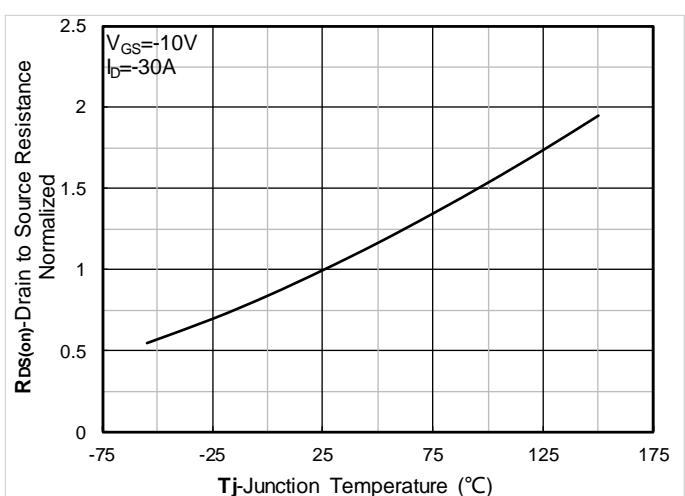
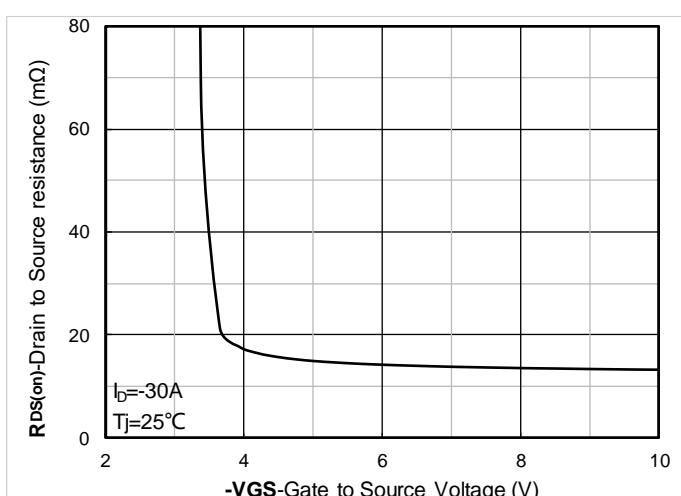
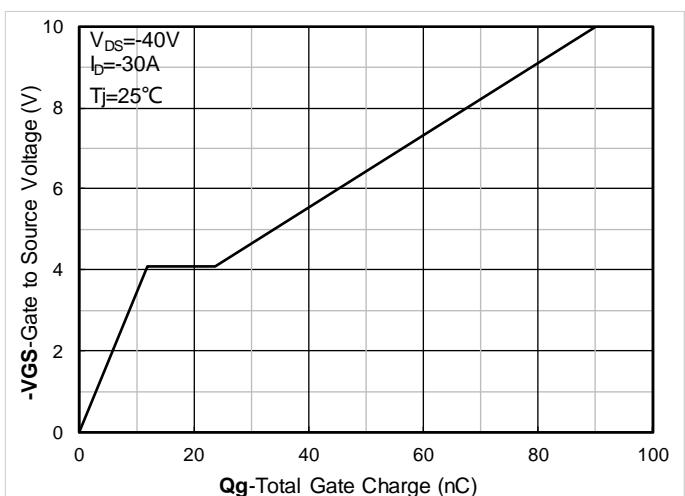
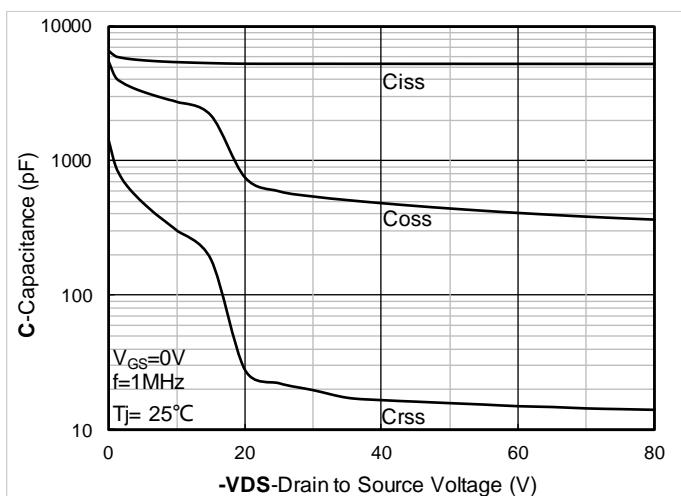
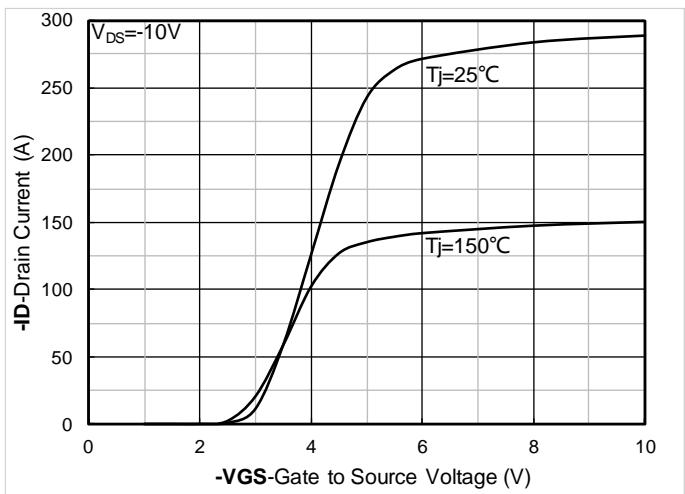
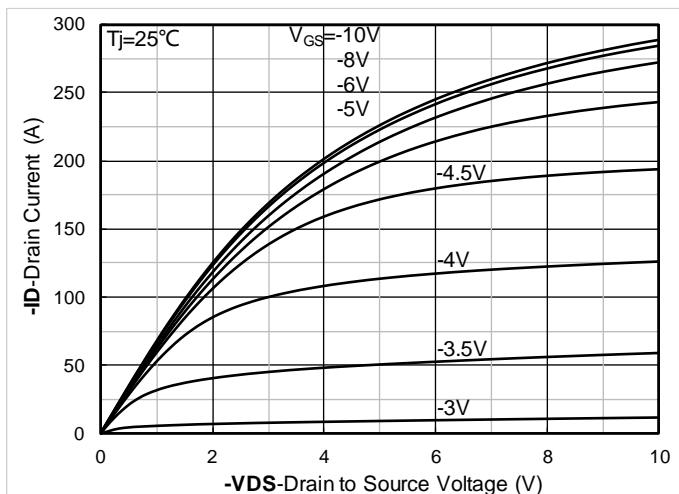


Figure 5. On-Resistance vs Gate to Source Voltage; typical values

Figure 6. Normalized On-Resistance

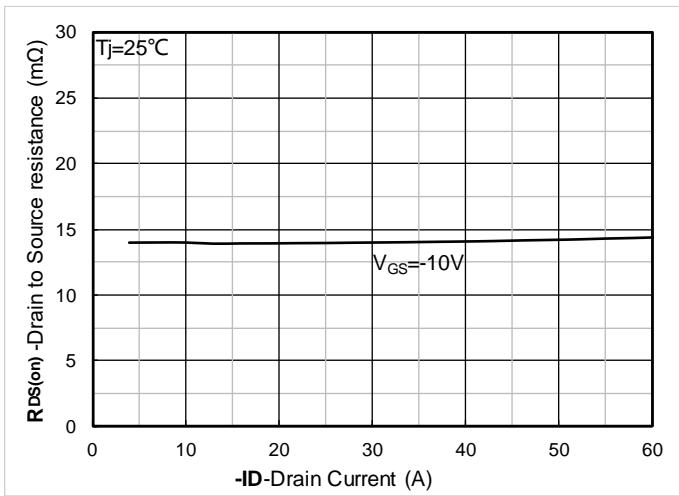


Figure 7.  $R_{DS(on)}$  VS Drain Current; typical values

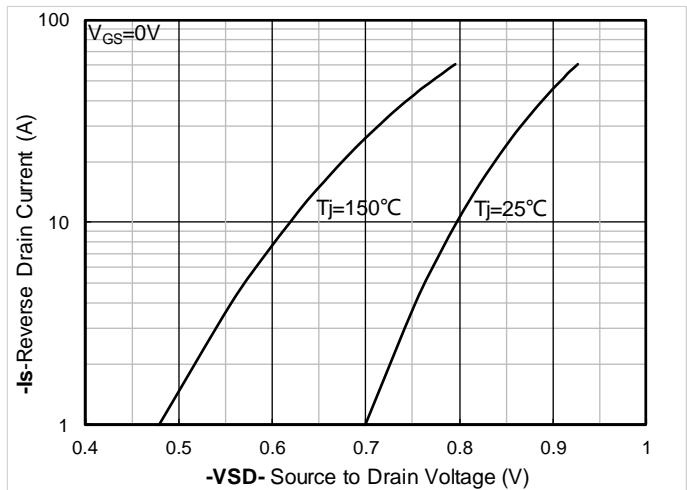


Figure 8. Forward characteristics of reverse diode; typical values

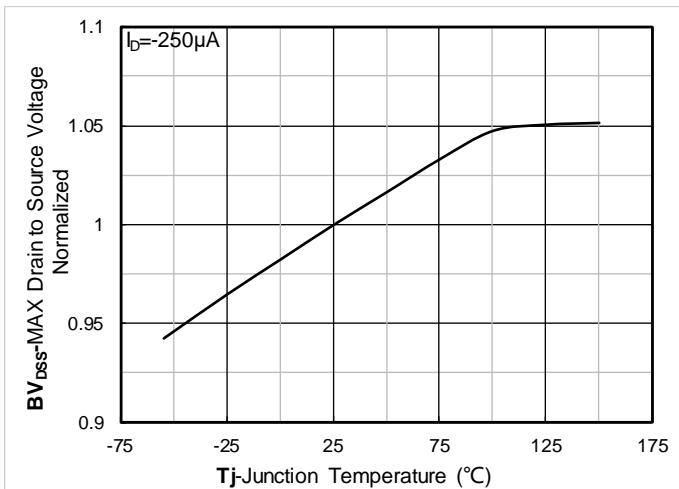


Figure 9. Normalized breakdown voltage

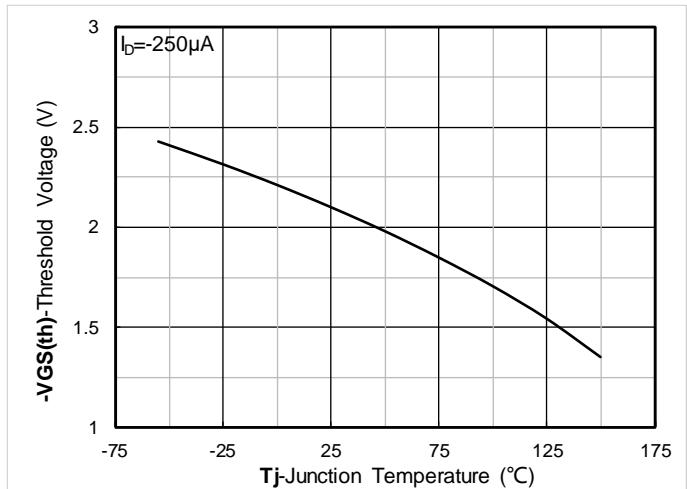


Figure 10. Gate Threshold voltage; typical values

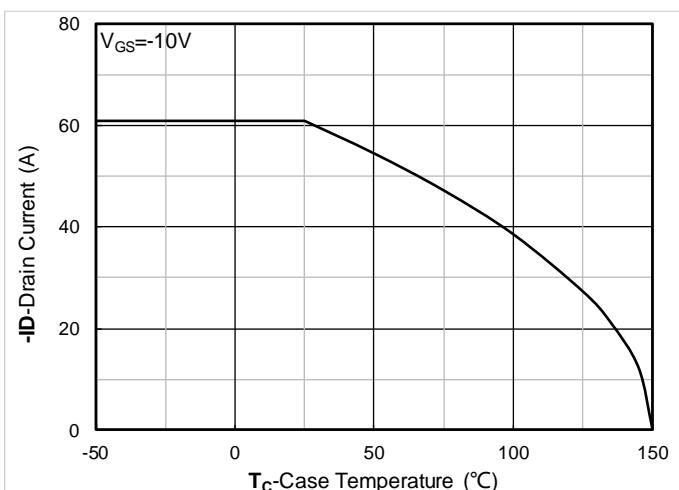


Figure 11. Current dissipation

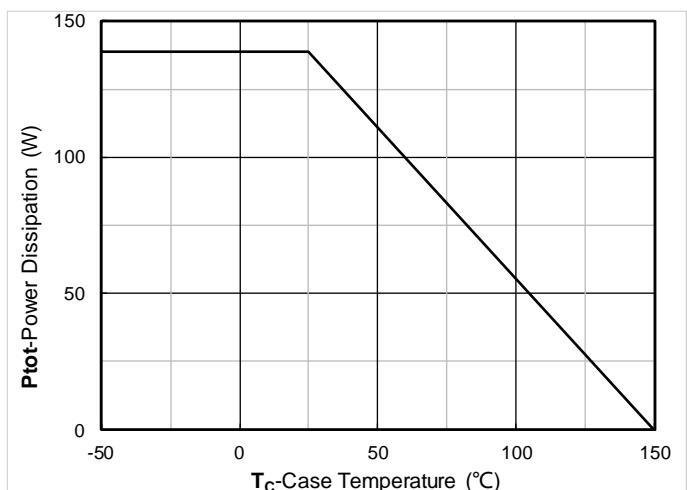


Figure 12. Power dissipation

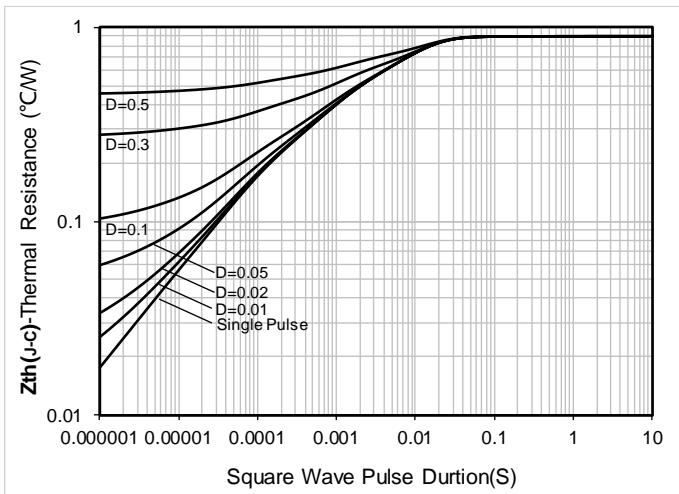


Figure 13. Maximum Transient Thermal Impedance

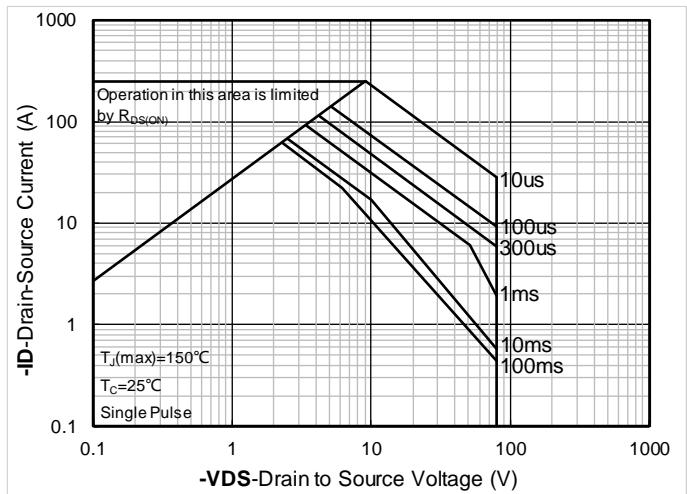
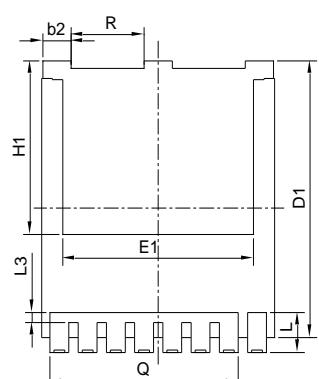
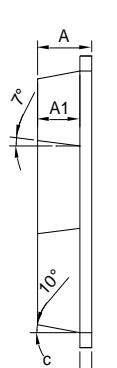
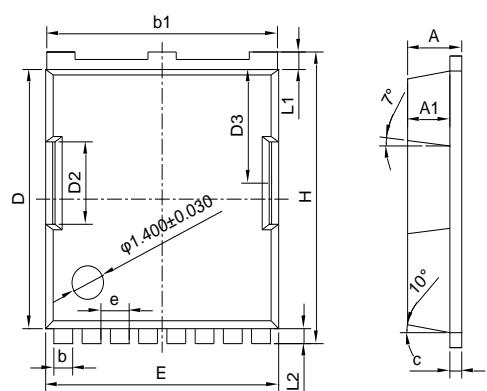


Figure 14. Safe Operation Area



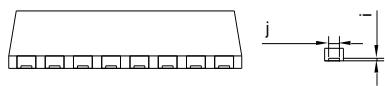
## ■ TOLL Package information



TOP VIEW

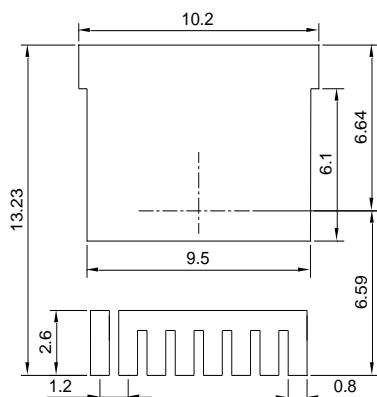
SIDE VIEW

BOTTOM VIEW



## Note:

1. Controlling dimension: in millimeters.
2. General tolerance:  $\pm 0.03\text{mm}$ .
3. The pad layout is for reference purposes only.

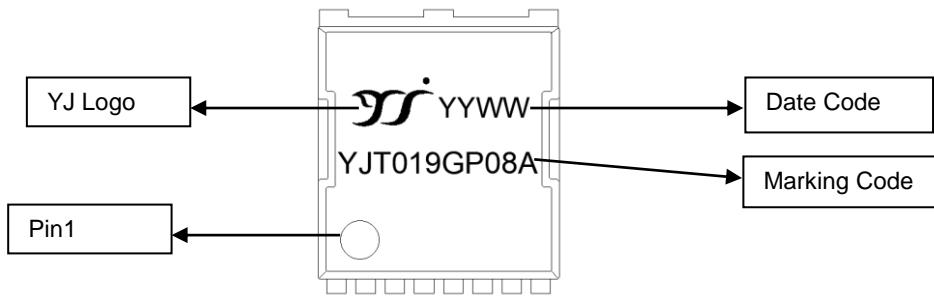
SUGGESTED SOLDER PAD LAYOUT  
TOP VIEW

SYMBOL	MILLIMETER		
	MIN	NOM	MAX
A	2.2	2.3	2.4
A1	1.7	1.8	1.9
b	0.7	0.8	0.9
b1	9.7	9.8	9.9
b2	1.1	1.2	1.3
c	0.4	0.5	0.6
D	10.28	10.38	10.48
D1	10.98	11.08	11.18
D2	3.2	3.3	3.4
D3	4.45	4.55	4.65
E	9.8	9.9	10
E1	8	8.1	8.2
e		1.2 BSC	
H	11.58	11.68	11.78
H1		6.95 BSC	
i		0.1 REF	
j		0.46 REF	
L	1.5	1.6	1.7
L1	0.6	0.7	0.8
L2	0.5	0.6	0.7
L3	0.3	0.4	0.5
Q		8 REF	
R	3.0	3.1	3.2

UNIT: mm



## ■ Marking Information



Note:

1. All marking is at middle of the product body
2. All marking is in laser printing
3. YJT019GP08A is marking code, YYWW is date code, "YY" is year, "WW" is week
4. Body color: Black



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