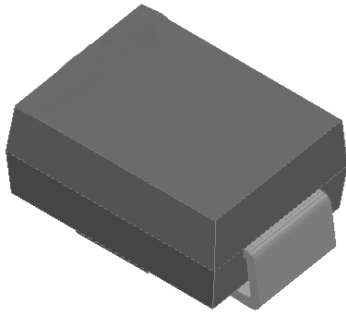


Thyristor Surge Suppressors



Features

- Low switching voltage
- Low on-state voltage
- Does not degrade surge capability after multiple surge events within limit
- Fails short circuit when surged in excess of ratings
- Low Capacitance

Typical Applications

- Protect circuit

Mechanical Data

- **Package:** DO-214AA (SMB)
Molding compound meets UL 94 V-0 flammability rating, RoHS-compliant, halogen-free
- **Terminals:** Tin plated leads, solderable per J-STD-002 and JESD22-B102

■Maximum Ratings (T_a=25°C Unless otherwise specified)

PARAMETER	SYMBOL	UNIT	Max
Storage temperature	T _{stg}	°C	-55~+150
Junction temperature	T _j	°C	-55~+150
Thermal resistance(Typical)	R _{θJ-A}	°C/W	80
	R _{θJ-C}	°C/W	4.5

■Thermal Characteristics (T_a=25°C Unless otherwise specified)

Part Number	VDRM @I _{drm} =5μA (V)	VS @100V/μs (V)	VT @It=2.2Amps (V)	IDRM @V _{drm} (μA)	IS (mA)	IT (A)	IH (mA)	Co (pF) @1MHz,2V bias
	Min	Max	Max	Max	Max	Max	Min	MAX
P0080SD	6	15	4	5	800	2.2	50	150

Notes:

(1): Absolute maximum ratings measured at T_A = 25°C (unless otherwise noted)

■Surge ratings, see table below.

Series	I _{pp} 8/20us (A)	I _{pp} 10/1000us (A)	V _{pp} 10/700μs 40Ω (KV)
	Min	Min	Min
D	500	200	8



■ Characteristics (Typical)

FIG1:V-I Characteristics

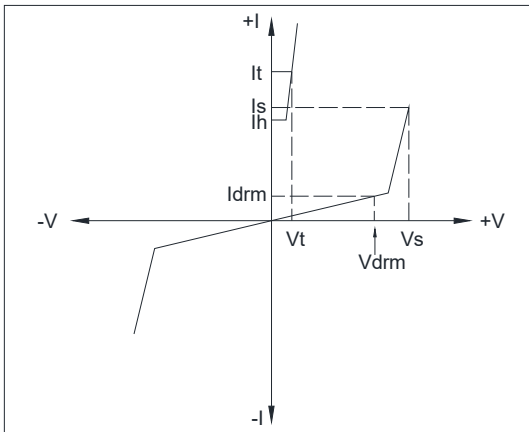


FIG2: tr*td Pulse Wave-form

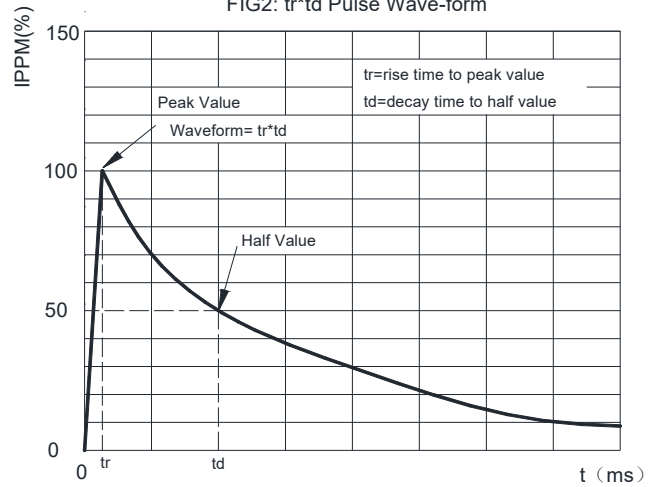


FIG3:Normalized Vs Change vs Junction Temperature

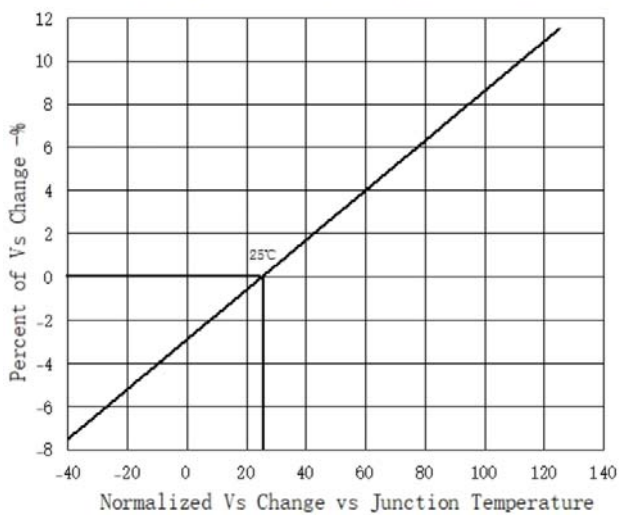
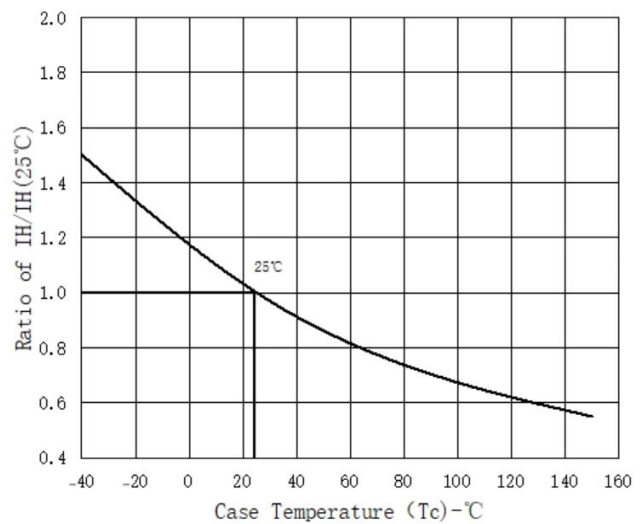


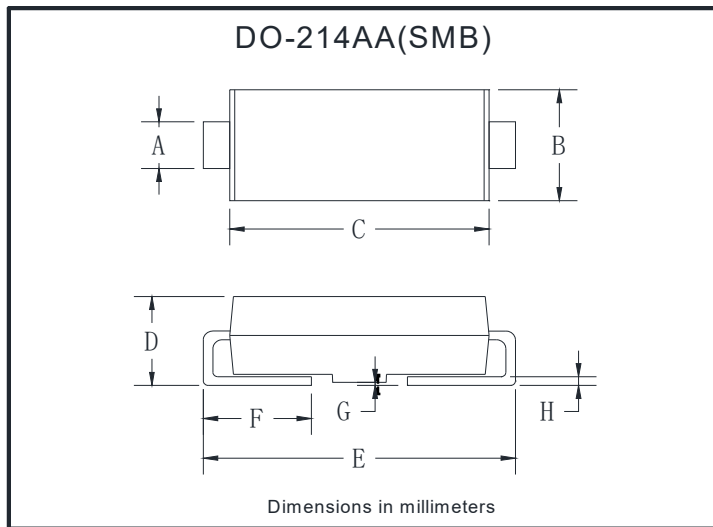
FIG4:Normalized DC Holding Current vs Case Temperature



■ Ordering Information (Example)

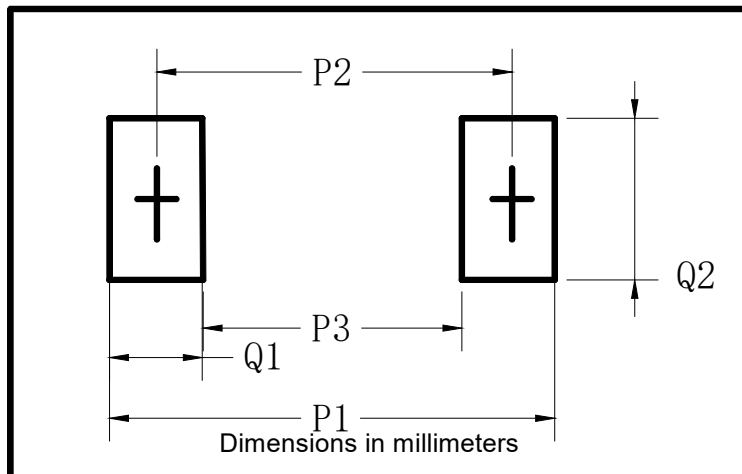
PREFERRED P/N	PACKING CODE	UNIT WEIGHT(g)	MINIMUM PACKAGE(pcs)	INNER BOX QUANTITY(pcs)	OUTER CARTON QUANTITY(pcs)	DELIVERY MODE
P0080SD	F1	Approximate 0.0975	3000	6000	48000	13" reel

■ Outline Dimensions



DO-214AA(SMB)		
Dim	Min	Max
A	1.85	2.20
B	3.30	3.94
C	4.05	4.75
D	1.95	2.61
E	5.21	5.59
F	0.76	1.52
G	0.05	0.20
H	0.15	0.35

■ Suggested pad layout



DO-214AA(SMB)	
Dim	Millimeters
P1	6.8
P2	4.3
P3	1.8
Q1	2.5
Q2	2.3



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